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INFORMATION DISCLOSURE STATEMENT BY APPLICANT		Application Number	TBA; Continuation of SN 10/315,030
(use as many sheets as necessary)		Filing Date	herewith
		First Named Inventor	Tomoharu Tanaka
		Group Art Unit	2824 (parent)
		Examiner Name	Son T. Dinh (parent)
Sheet	1	of	2
		Attorney Docket Number	001701.00193

U.S. PATENT DOCUMENTS

FOREIGN PATENT DOCUMENTS

FOREIGN PATENT DOCUMENTS						
Examiner Initials*	Cite No. ¹	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	Text
		Country Code ³ - Number ⁴ - Kind Code ⁵ (if known)				
SN		JP 08-279297	10-22-1996	Samsung Electron Co Ltd.		Abstr.
SN		JP 07-230695	08-29-1995	Toshiba Corp.		Abstr.

Examiner Signature		Date Considered	11/11/04
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³ Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). ⁴ For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. ⁵ Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possible. ⁶ Applicant is to place a check mark here if English language Translation is attached.

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**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

(use as many sheets as necessary)

Sheet

2

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Compl. te if Known

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Examiner Name	Son T. Dinh (parent)

Attorney Docket Number 001701.00193

OTHER PRIOR ART -- NON PATENT LITERATURE DOCUMENTS

Examiner Initials *	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
GD		Tae-Sung JUNG et al.; "A3.3V 128 Mb Multi-Level NAND Flash Memory for Mass Storage Applications," ISSCC Digest of Technical papers, pp. 32-33, Feb. 1998	
GD		Tae-Sung JUNG et al.; "A 117-mm ² 3.3-V 128-Mb Multilevel NAND Flash Memory For Mass Storage Applications," IEEE Journal of Solid-State Circuits, Vol. 31, No. 11, November 1996, pp. 1575-1583	
GD		Young, Joon Choi et al.; "A High Speed Programming Scheme For Multi-Level NAND Flash Memory," Samsung Electronics Co., Ltd., Kiheung, Korea, 1996 Symposium on VLSI Circuits Digest of Technical Papers, pp. 170-171	

Examiner Signature

Date Considered

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